Code No.: 13352 S N/O

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS), HYDERABAD

Accredited by NAAC with A++ Grade

B.E. (E.E.E.) III-Semester Supplementary Examinations, August-2023 **Electronics Engineering-I**

Time: 3 hours

Max. Marks: 60

Note: Answer all questions from Part-A and any FIVE from Part-B

Q. No.	Stem of the question	M	L	CO	PO	PSC
1.	A Germanium diode has reverse saturation current of 30 μ A at 125°C. What is reverse resistances for a bias 0.3 V at this temperature?		1	1	2	2
2.	Write ideal values of rectifier parameters.	2	1	2	1	2
3.	Explain briefly early effect concept in BJT.	2	1	1	1	2
4.	Explain the significance of operating point.	2	1		1	
5.	List out the parameters of hybrid model.	2	1	1		2
6.	Compare CB and CE amplifier configurations.		1	4	1	2
7.	Define the pinch-off voltage Vp and Sketch the depletion region before and after pinch-off in N channel JFET.	2	1	2	1	2
8.	An N channel JFET has I_{DSS} =8mA and V_p =-6V. Determine the I_{DS} for V_{gs} =-2V in the pinch-off region.	2	4	2	2	2
9.	Define hold current and latch current in SCR.	2	1	3	1	2
10.	Mention the procedure how frequency can be measured using a CRO.	2	2	3	1	2
	Part-B $(5 \times 8 = 40 Marks)$					
	A. silicon single phase full wave bridge rectifier circuit is shown below. Explain what happens if the transformer and the load positions are interchanged?	4	3	2	2	2
b) 2	A Si diode indicates forward current of 2 mA and 10 mA, when diode					
	coltages are 0.6 V and 0.7 V respectively. Estimate the operating emperature of the diode junction.	1 2	2	1	2	2

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12. a)	Briefly explain the operation of fixed bias circuit using BJT.	4	2	1	1	2
- 1		4	4	4	2	2
13. a)	A CE amplifier has the h-parameters given by hie= 1000Ω ,hre= $2x10$ -4,hfe= 50 and hoe= 25μ mhos.If both load and source resistances are $1k\Omega$.Determine the i)Current gain ii)Input impedance ii)Voltage gain iv)Output Impedance.	4	4	4	2	2
b)	Justify how h-parameter exact model of bipolar transistor can be converted to simplified model?	4	3	4	2	2
14. a)	An N-channel JFET has IDSS = 12 mA and the transconductance of 4 mA/V when VGS = 0 Volts.	4	4	5	2	2
	 i) Find the pinch off voltage, Vp. ii) Find the drain current when VGS = -2 volts 					
b)	Explain the operation of N- Channel Enhancement mode MOSFET and Draw VI-Characteristics.	4	2	5	1	2
15. a)	Draw and discuss SCR V-I characteristics.	4	2	3	1	2
b)	Explain the working principle of Light Emitting Diode.	4	2	3	1	2
16. a)	A half wave rectifier has a load of $3.5 \mathrm{K}\Omega$. If the diode resistance and the secondary coil resistance together have a resistance of 800Ω and the input voltage has a signal voltage of peak value 240V, Calculate: a) peak, average and rms value of current b) d.c. power output c) a.c. power input d) efficiency and ripple factor of the rectifier.	4	4	2	2	2
b)	C DNI innation Diode with the help of VI-	4	2	1	1	2
17.	Answer any two of the following:					
a)	A CE amplifieris driven by a voltage source of internal resistance $RS=800\Omega$ and the load impedance is $RL=1000\Omega$ the h-parameters are hie=1k Ω ,hre=2x10-4hfe=50 and hoe=25 μ mhos.Compute the Current gain A_I ,Input resistance R_I ,Voltage gain A_V and Output resistance R_O .		4	4	2	2
b	Compare between BJT and JFET.	4	2	1	1	2
	Explain the V-I Characteristics of Tunnel diode with the help of energy band diagrams.	4	2	3	1	2

M: Marks; L: Bloom's Taxonomy Level; CO; Course Outcome; PO: Programme Outcome

• ` `	Blooms Taxonomy Level – 1	20%
1)	Blooms Taxonomy Level – 2	42.5%
11)	Blooms Taxonomy Level – 3 & 4	37.5%
iii)	Blooms Taxonomy Dever	